

## ABSTRACT OF THE DISCLOSURE

In a semiconductor integrated circuit device, a plurality of tristate buffers provided corresponding to a plurality of internal signals respectively and receiving at its input node the corresponding internal signal are  
5 arranged distributed along the direction of extension of a tristate bus, and any of the plurality of tristate buffers is selectively activated, so that a desired internal signal is externally taken. Therefore, lines for taking the internal signals are not concentrated, and therefore a small circuit area is sufficient.